Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	160	training near packet	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:15
L10	17825	ofdm	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:15
L11	5941	IFFT	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:15
L12	43798	preamble	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:15
L13	794	L10 and L11 and L12	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:15
L14	5	L9 and L13	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:20
L15	3428	L10 and (signal near4 noise near ratio)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:22
L16	1133	L15 and L11	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:23

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L17	321	L16 and L12	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/16 08:23
S1	10	"737339"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 10:47
S2	0	"737339.ap."	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:28
S3	17711	ofdm	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:28
S4	5927	IFFT	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON	2007/07/12 11:29
S5	43756	preamble	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:29
S6	793	S3 and S4 and S5	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:30
S7	. 3	S6 and (hermitian near4 symmetry)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:46

S11	21	"0239177"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:44
S12	310	S6 and @ad<"20031216"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:48
S13	14669	polyphase	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:48
S14	310	S12 and S3	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 11:49
S15	25	S14 and Hermitian	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 17:03
S16	14	"6026074"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 15:37
S17	600	370/210.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 17:13
S18	31	S17 and hermitian	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 17:08

S19	14340	channel near estim\$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 17:09
S20	208	S19 and S14	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/12 17:14
S21	1053	370/208.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON ·	2007/07/12 17:13
S22	55	S21 and S14	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON	2007/07/16 08:14



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Day: Monday Date: 7/16/2007

Time: 09:03:02

Inventor Name Search Result

Your Search was:

Last Name = FANG

First Name = YUGUANG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10721533	Not Issued	41		Multi-layer differential phase shift keying with bit-interleaved coded modulation and OFDM	FANG, YUGUANG
10737339	Not Issued	30		Channel estimation and synchronization with preamble using polyphase code	FANG, YUGUANG
11131103	Not Issued	30		Medium access control in wireless local area networks with multi-beam access point	FANG, YUGUANG M.
11080041	Not Issued	30			FANG, YUGUANG MICHAEL

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Inventor Name Search Result

Your Search was:

Last Name = KIM

First Name = BYUNG-SEO

Application#	Doton##	Status	Doto Filed	Title	Inventor Name
Application#	Patent#	Status	Date Flied	Title	Inventor Name
<u>10272670</u>	6794247	150	10/16/2002	METHOD OF FABRICATING A SEMICONDUCTOR MEMORY DEVICE HAVING RESISTOR	KIM, BYUNG-SEO
10721533	Not Issued	41		Multi-layer differential phase shift keying with bit-interleaved coded modulation and OFDM	KIM, BYUNG-SEO
10737339	Not Issued	30		Channel estimation and synchronization with preamble using polyphase code	KIM, BYUNG-SEO
10911157	Not Issued	61		Semiconductor memory device having resistor and method of fabricating the same	KIM, BYUNG-SEO
11238381	Not Issued	41	09/29/2005	Semiconductor memory device with stacked memory cell and method of manufacturing the stacked memory cell	KIM, BYUNG-SEO
11783531	Not Issued	25	04/10/2007	Phase-changeable memory device and method of manufacturing the same	KIM, BYUNG-SEO

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